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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/046,893
				Filing Date	January 17, 2002
				First Named Inventor	Osamu Nakamura et al.
				Group Art Unit	2823
				Examiner Name	H. Lee
Sheet	1 (List 1)	of	1	Attorney Docket Number	0756-2424



U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
Lee		6,204,154		Zhang et al.	03/20/2001	
		6,355,509		Yamazaki	03/12/2002	
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Examiner Signature	<i>Karin M. Lee</i>	Date Considered	12/31/03
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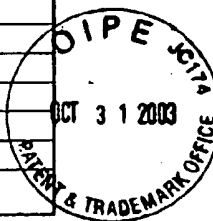
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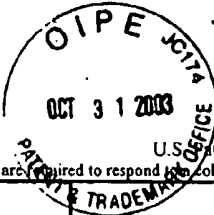
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/046,893
				Filing Date	January 17, 2002
				First Named Inventor	Osamu Nakamura et al.
				Group Art Unit	2823
				Examiner Name	H. Lee
Sheet	1 (List 2)	of	4	Attorney Docket Number	0756-2424



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		Number	Kind Code ² (if known)			
Lee		3,535,775		Garfinkel et al.	10/27/1970	
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				First Named Inventor	Osamu NAKAMURA et al.
				Group Art Unit	2823
				Examiner Name	H. Lee
Sheet	2 (List 2)	of	4	Attorney Docket Number	0756-2424

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		Number	Kind Code ² (if known)		
Lee		5,696,386		Yamazaki	12/09/1997
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				Examiner Name	H. Lee
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
Lee		JP	07-130652			05/19/1995		AB
		JP	07-183540			07/21/1995		AB
/		JP	08-078329			03/22/1996		AB
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Lee		Masayasu Miyake et al., <i>Characteristics of Buried-Channel pMOS Devices with Shallow Counter-Doped Layers Fabricated Using Channel Preamorphization</i> , IEEE Transactions on Electron Devices, Vol. 43, No. 3, March 1996, pp. 444-449	
Lee		Lurng S. Lee et al., <i>Argon Ion-Implantation on Polysilicon or Amorphous-Silicon for Boron Penetration Suppression in p+ pMOSFET</i> , IEEE Transactions on Electron Devices, Vol. 45, No. 8, August 1998, pp. 1737-1744	
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